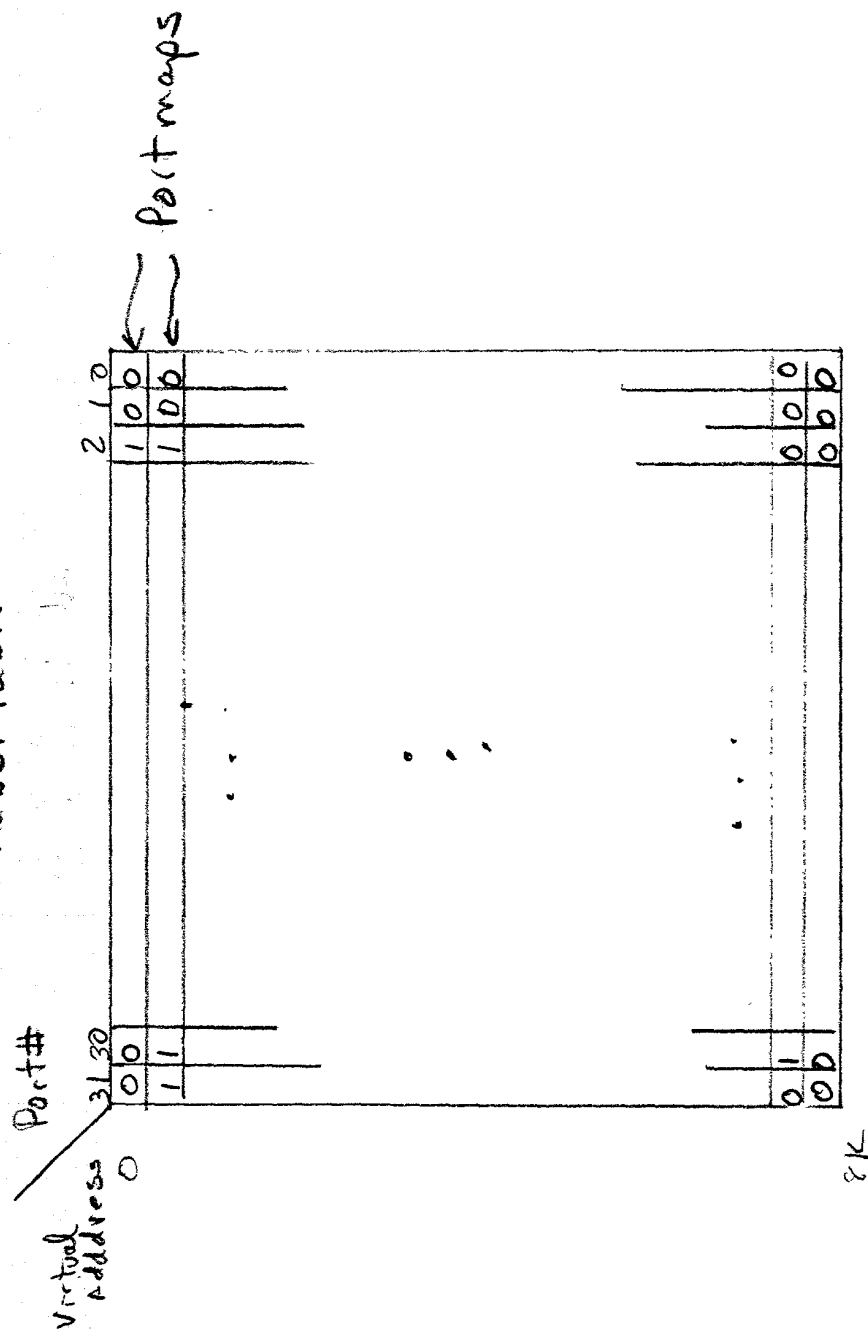


Fig 2

Label Table



M
L

Frame

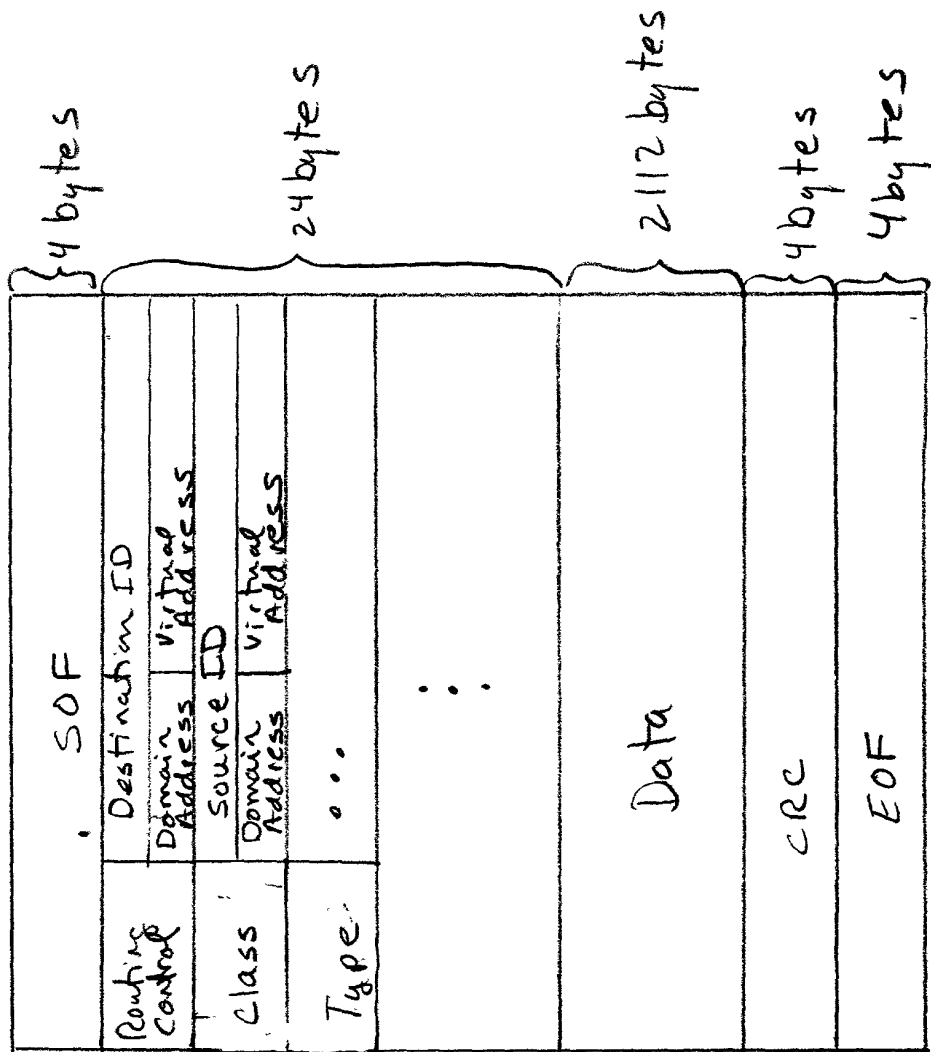


Fig 4

10068329-102601

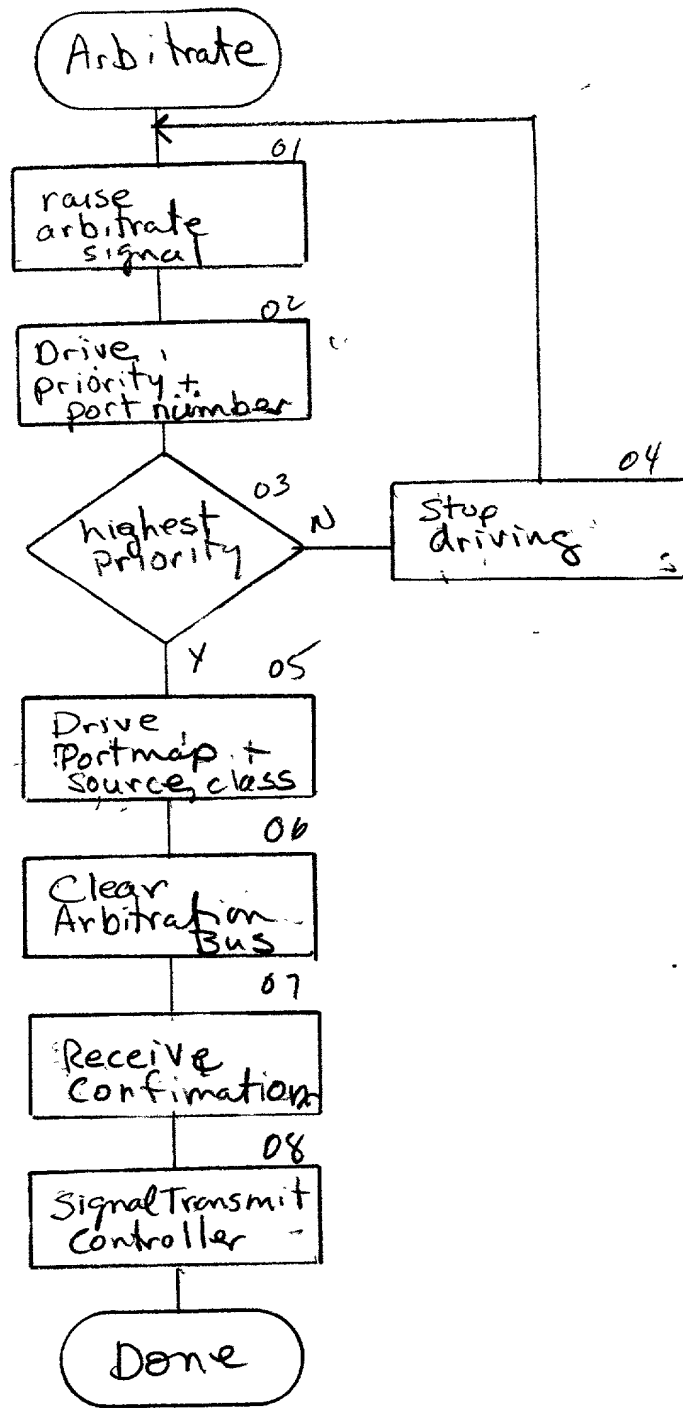


Fig 5

Transmission Controller

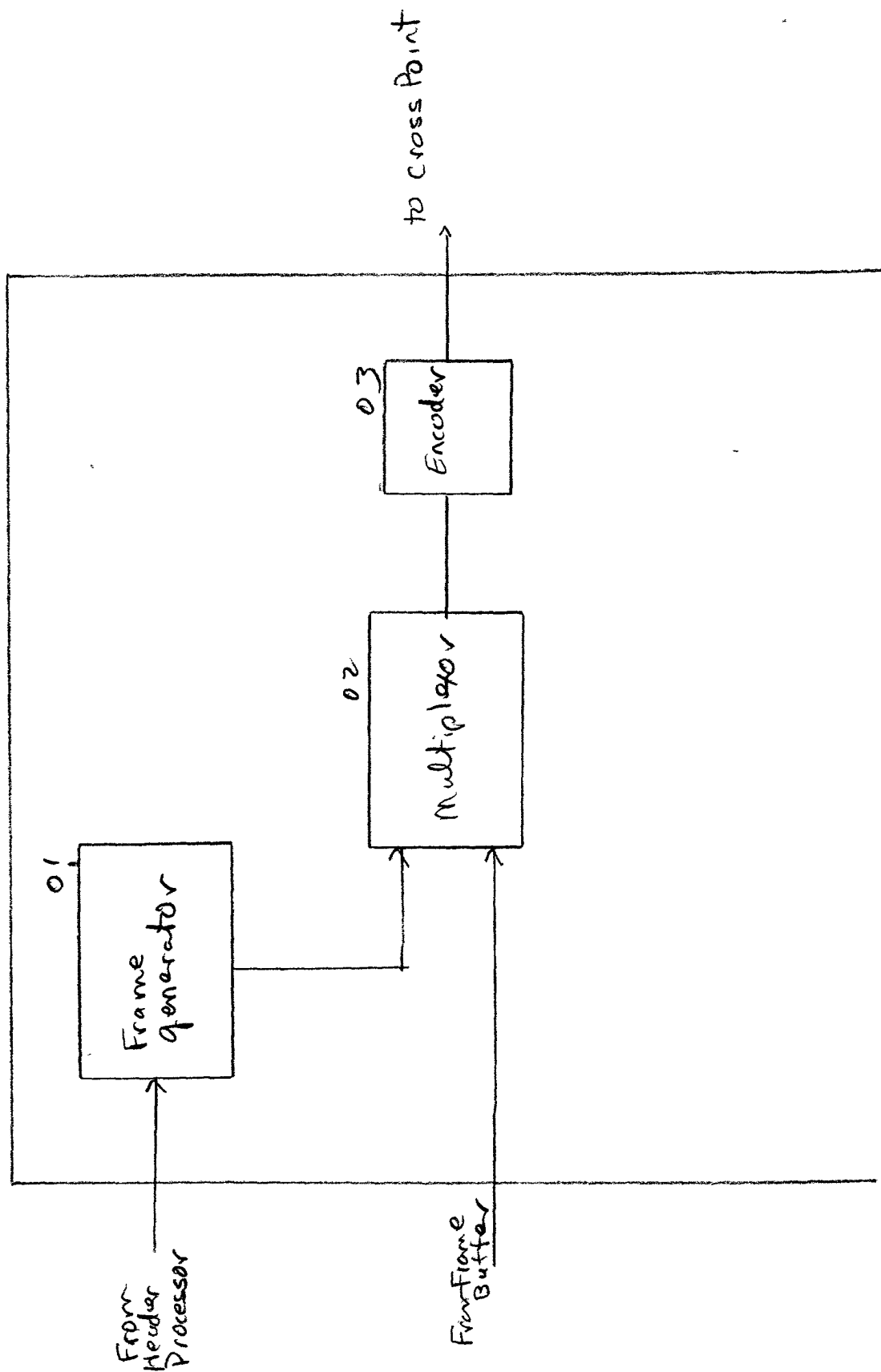


Fig 6

FIG. 1

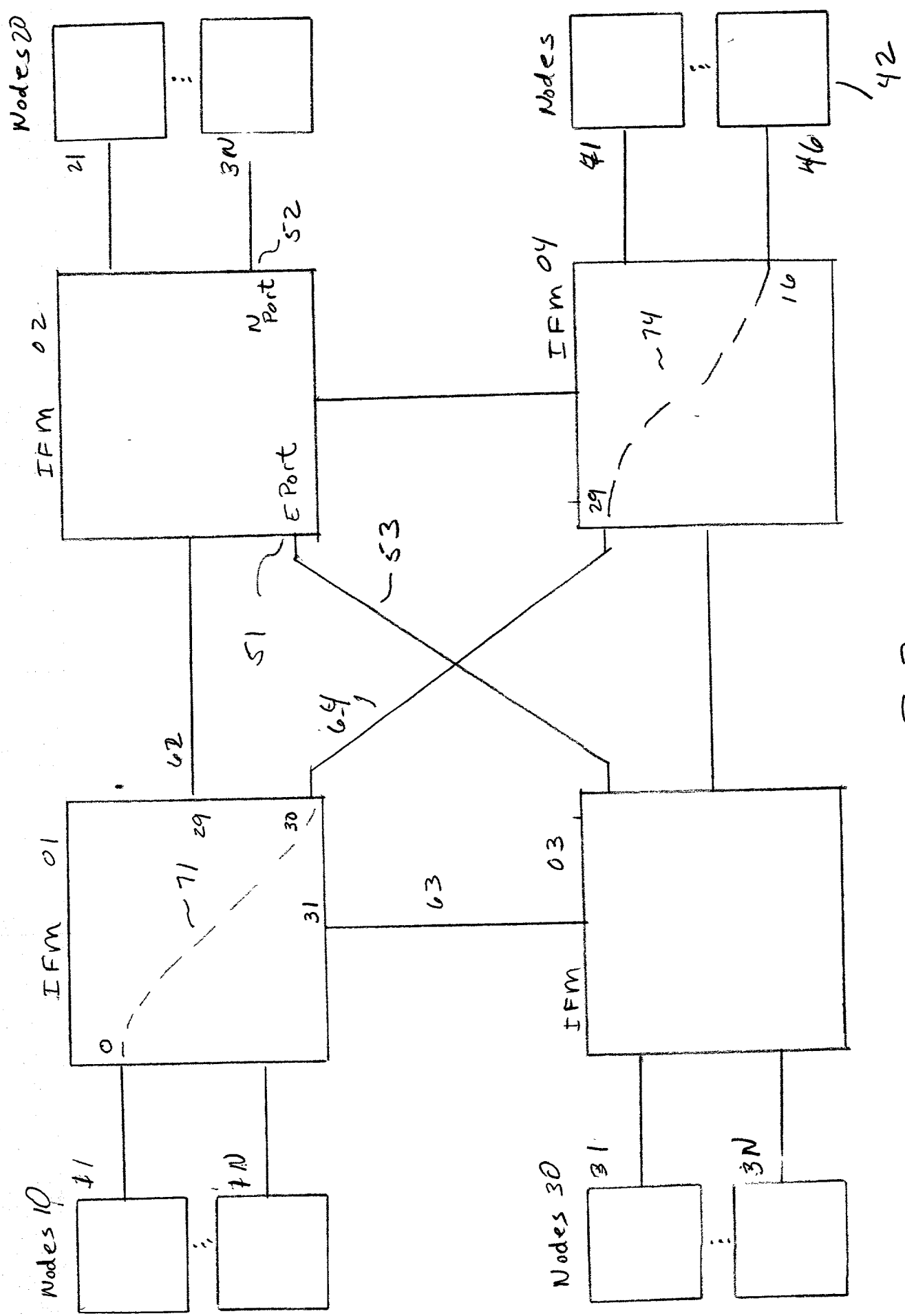


Fig 7

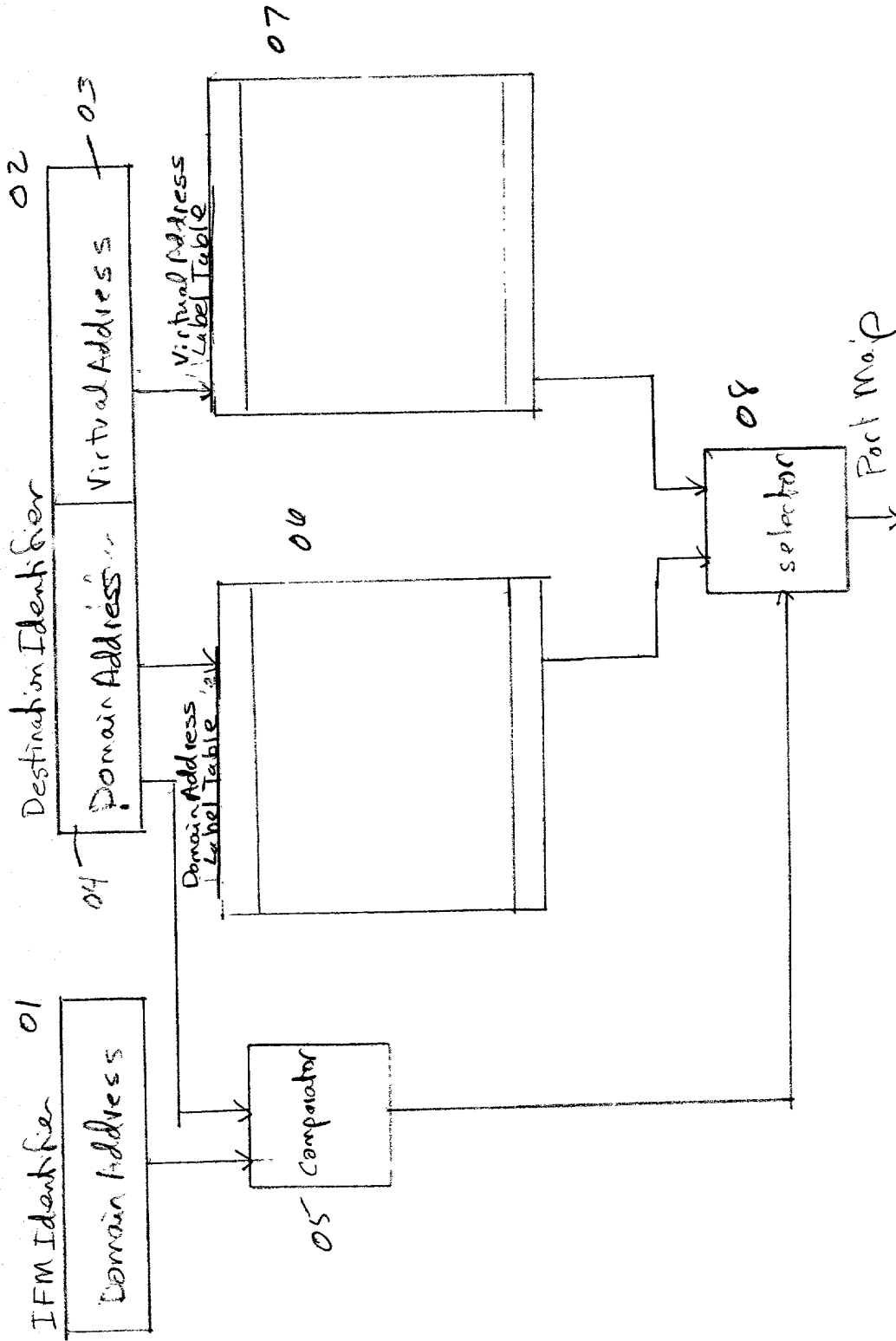


Fig. 8

Quad Switch Protocol Controller

00

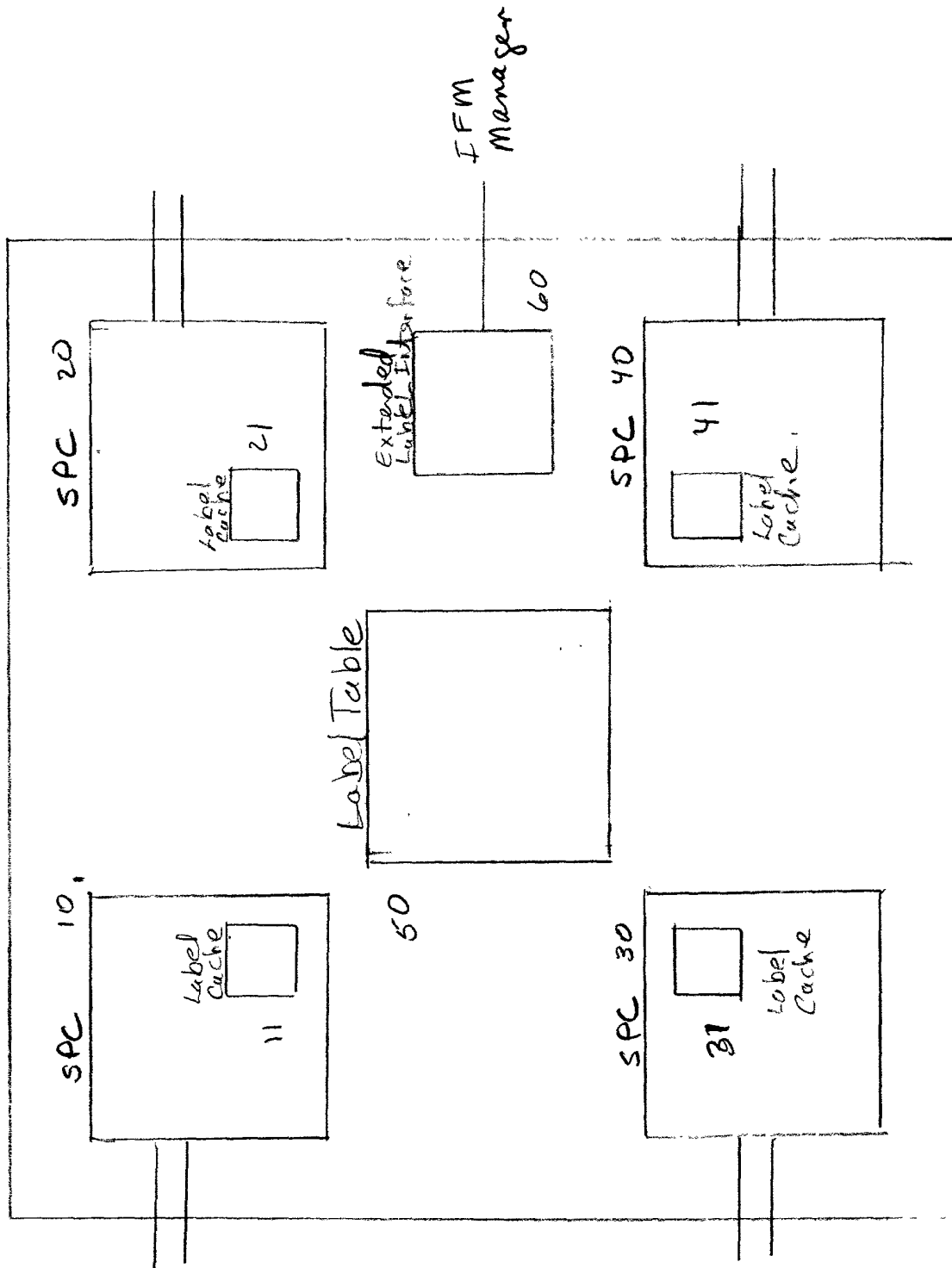


Fig. 9

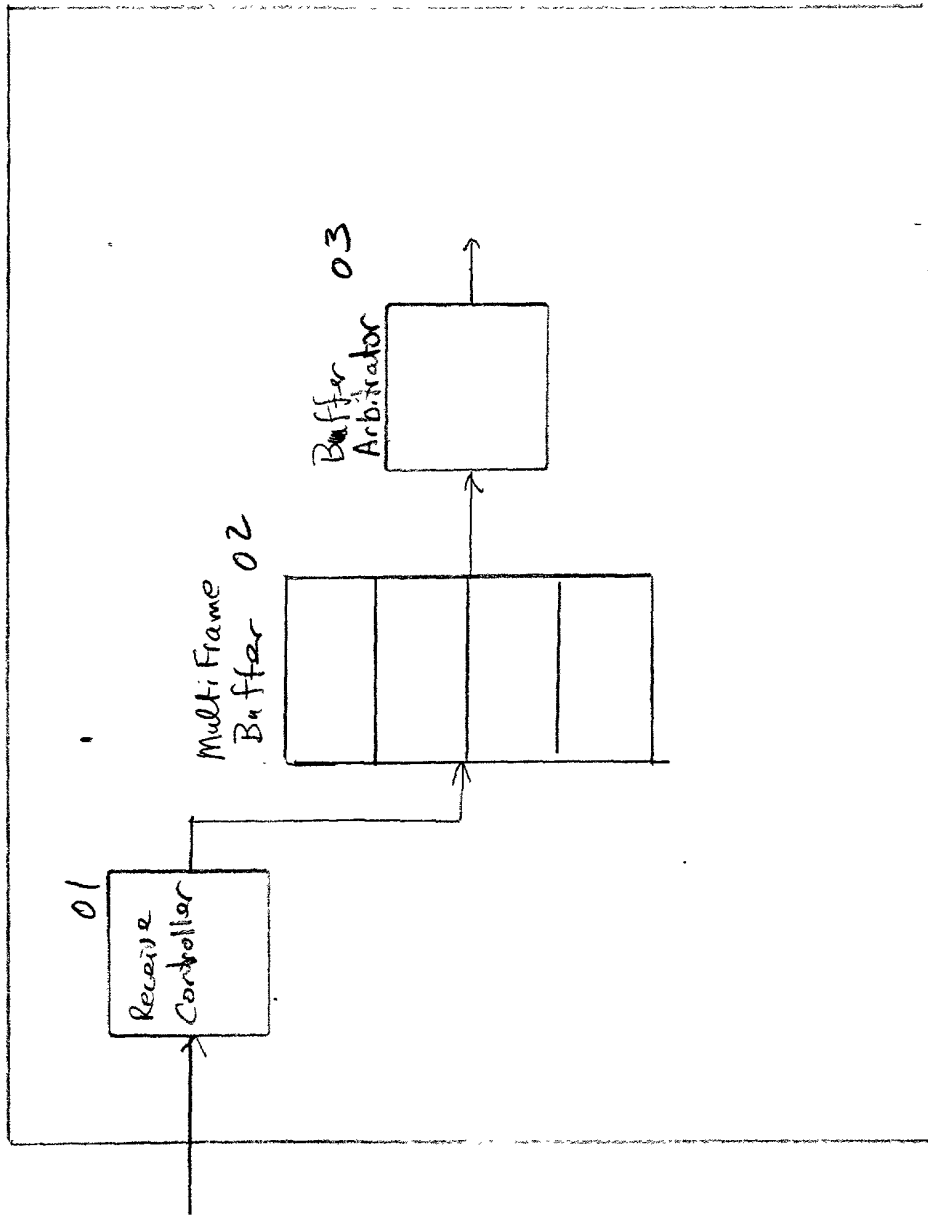


FIG 10

10063229-102601

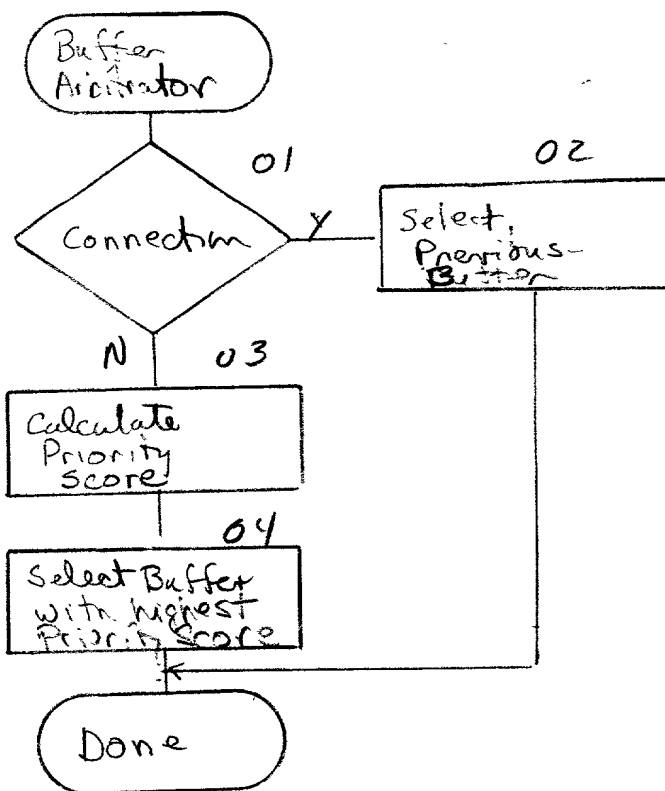


Fig 11

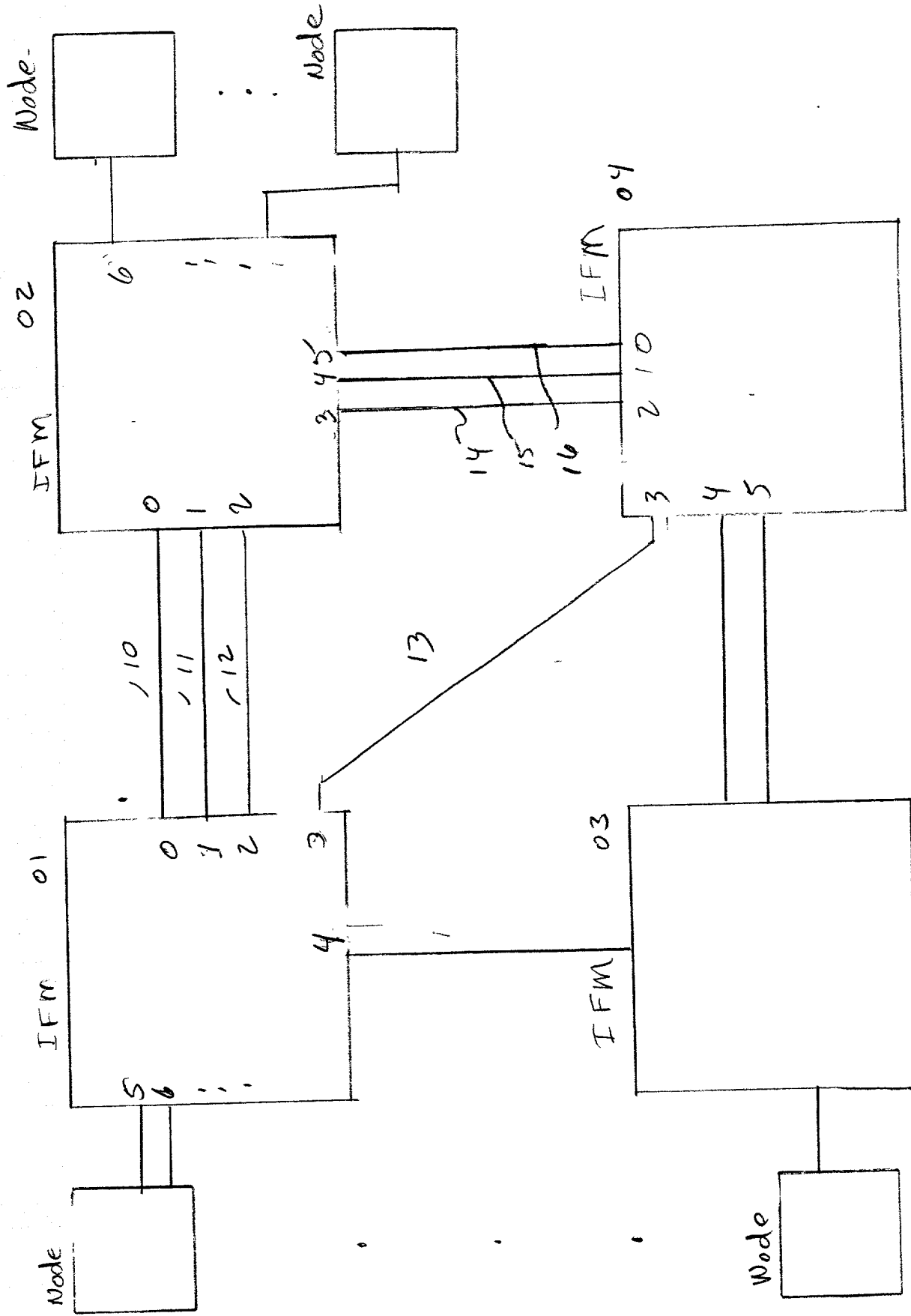


Fig 12

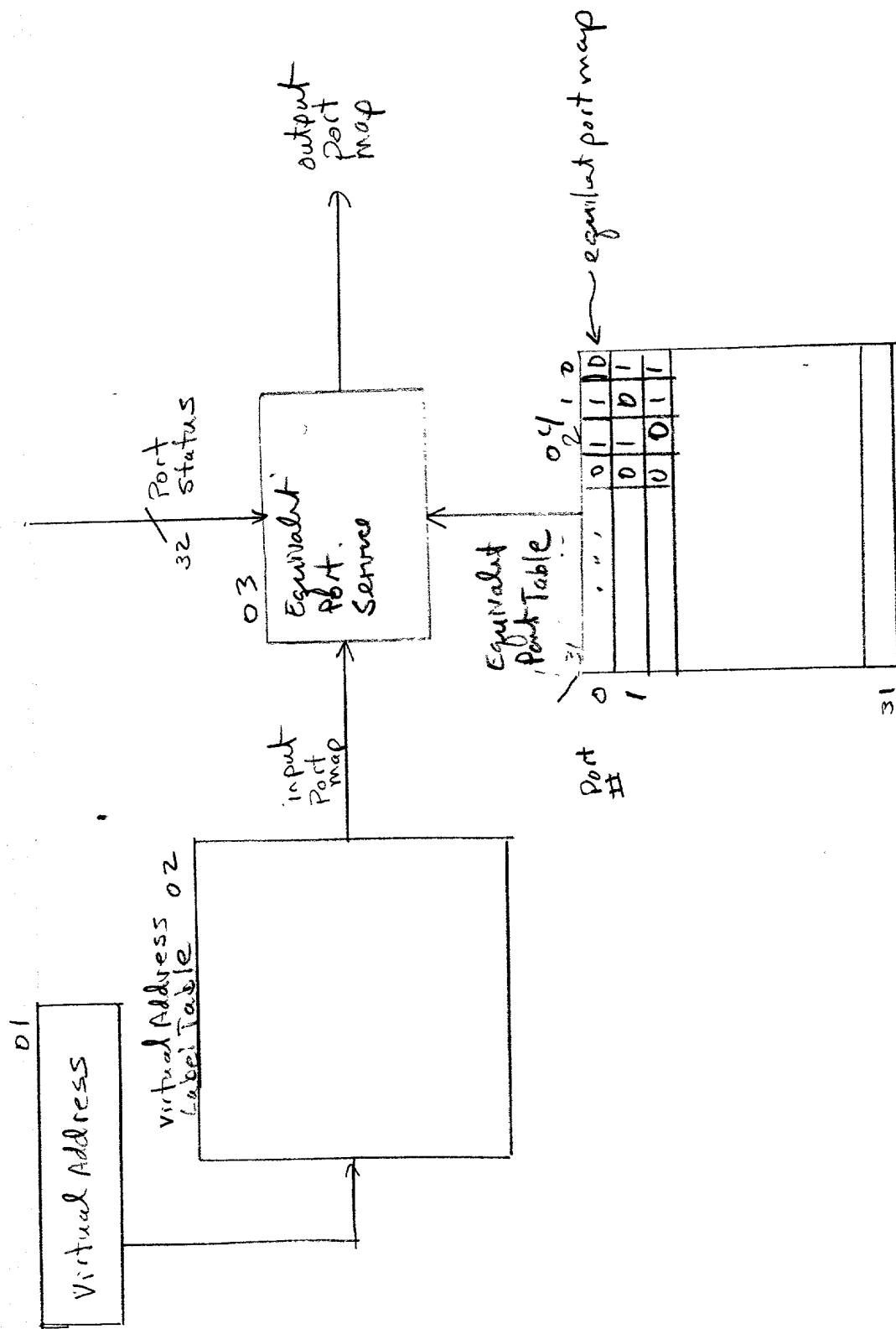


Fig 13

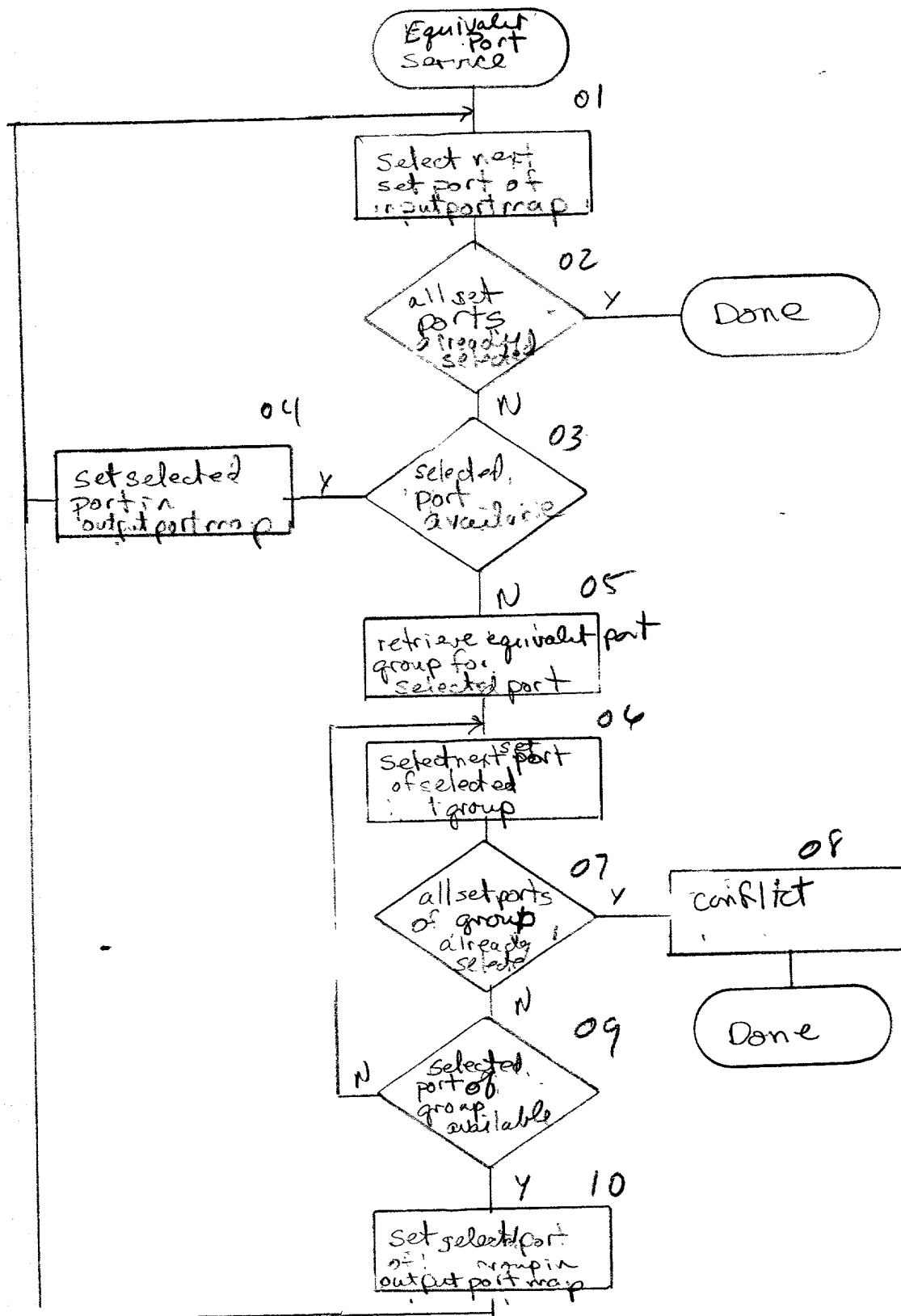


Fig 14

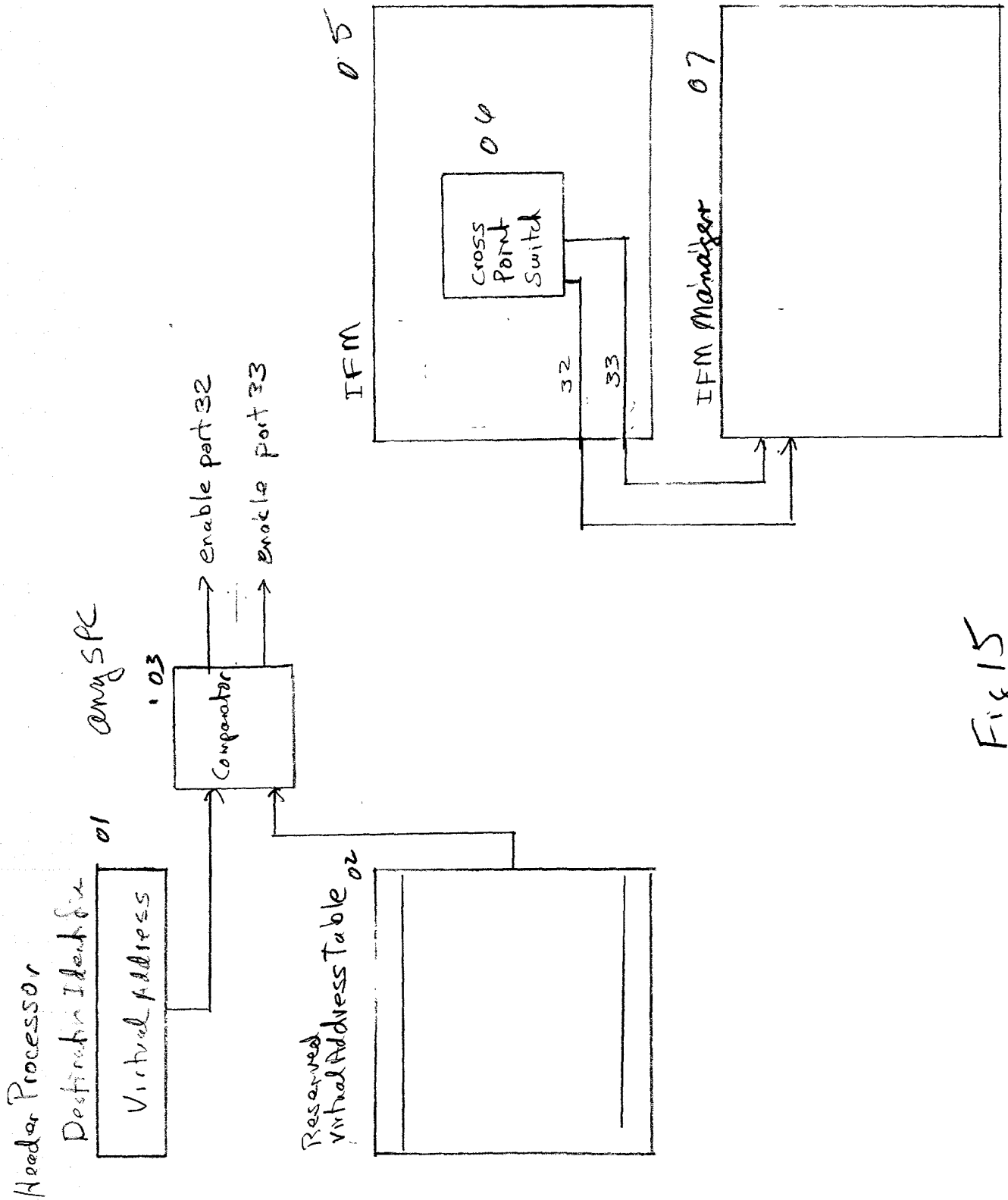
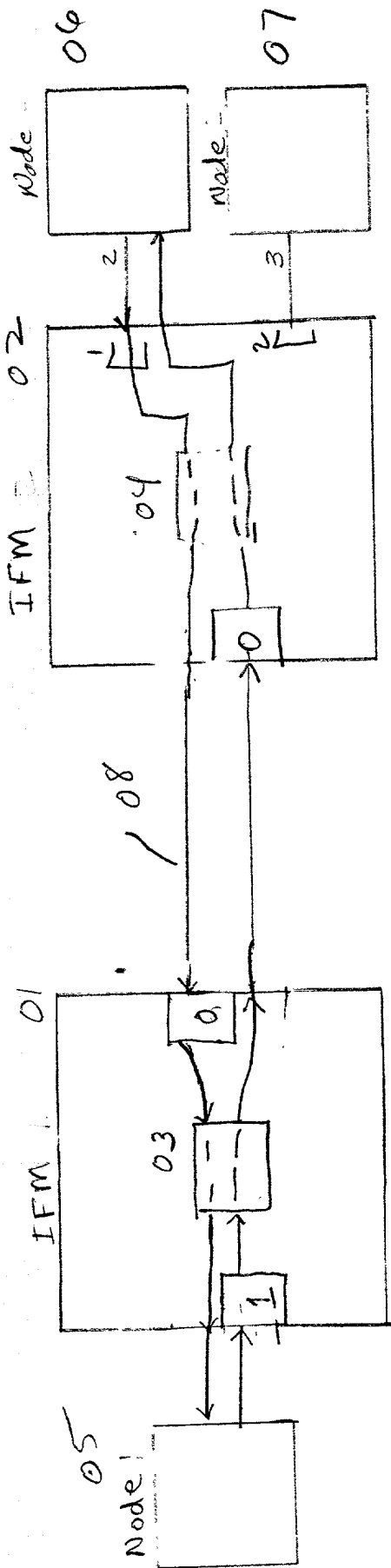


Fig 15



Dead lock

10

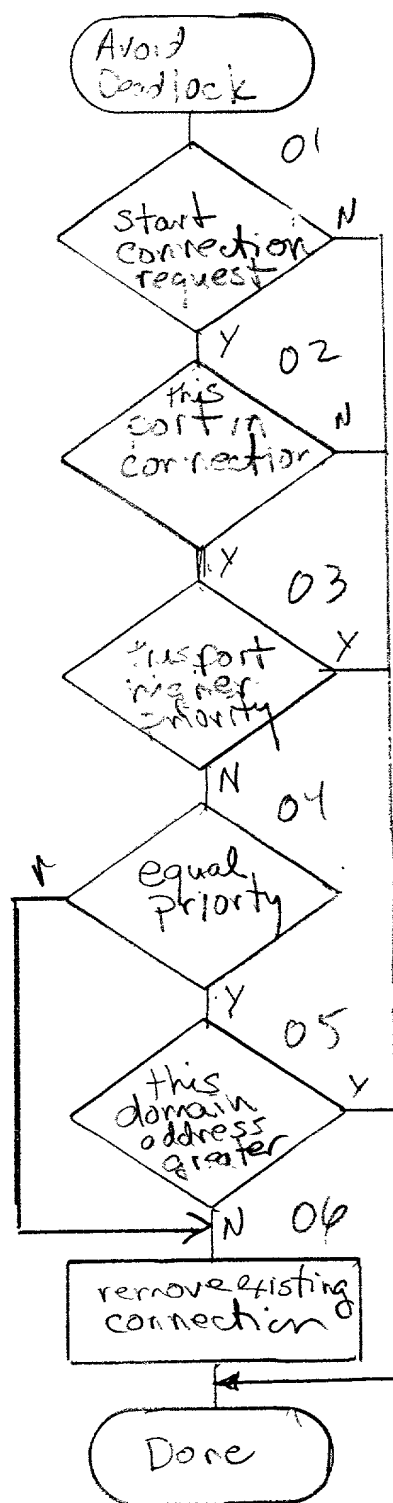
Time Node 1605 IFM 1601 Node 1606 IFM 1602

0 send start connect 1 2 send start connect

1	Connect 1 ↔ 0	Connect 2 ↔ 0
2	Forward start connect	Forward start connect
3	Can't forward start connect Node 1	Can't forward start connect

Fig 16

10068329-102601



When end-to-end
not established

Fig 17

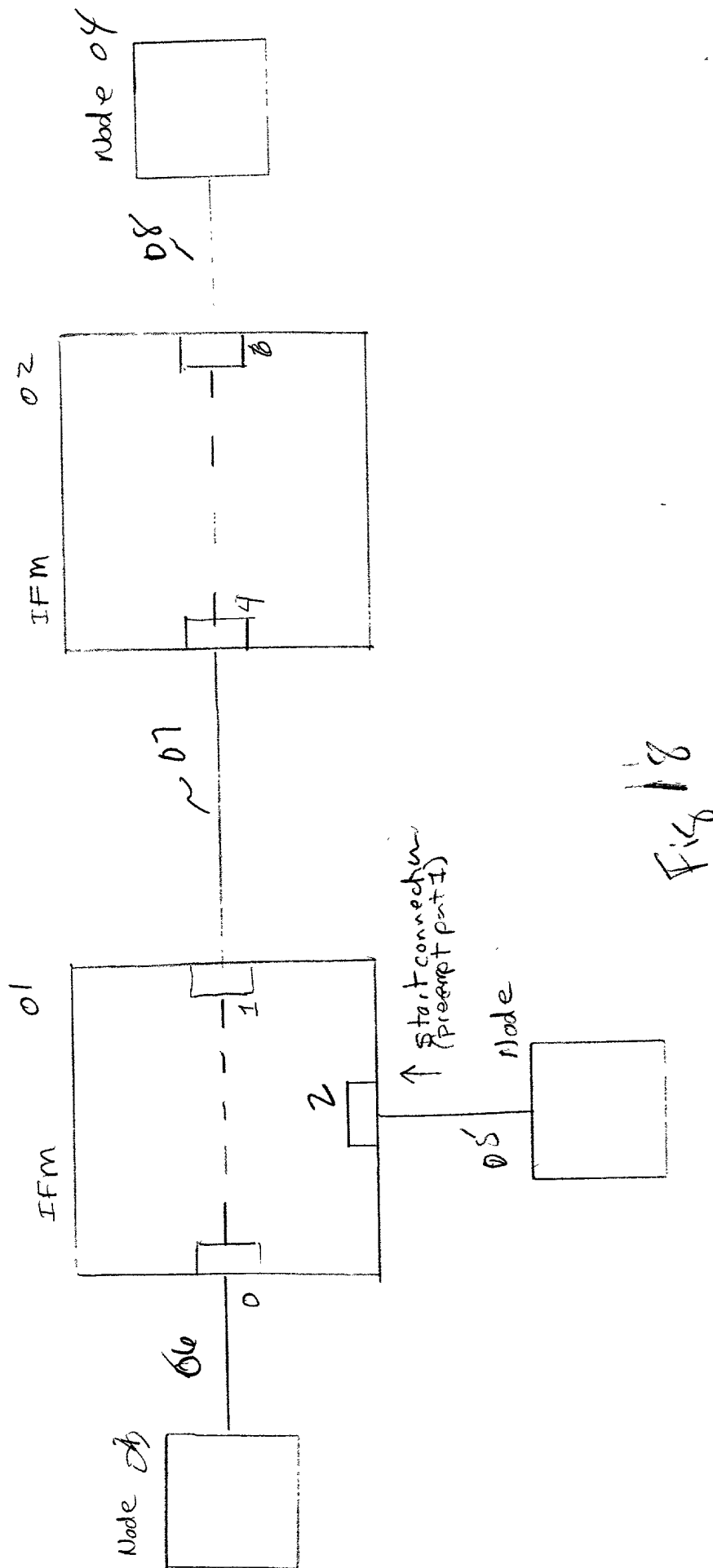
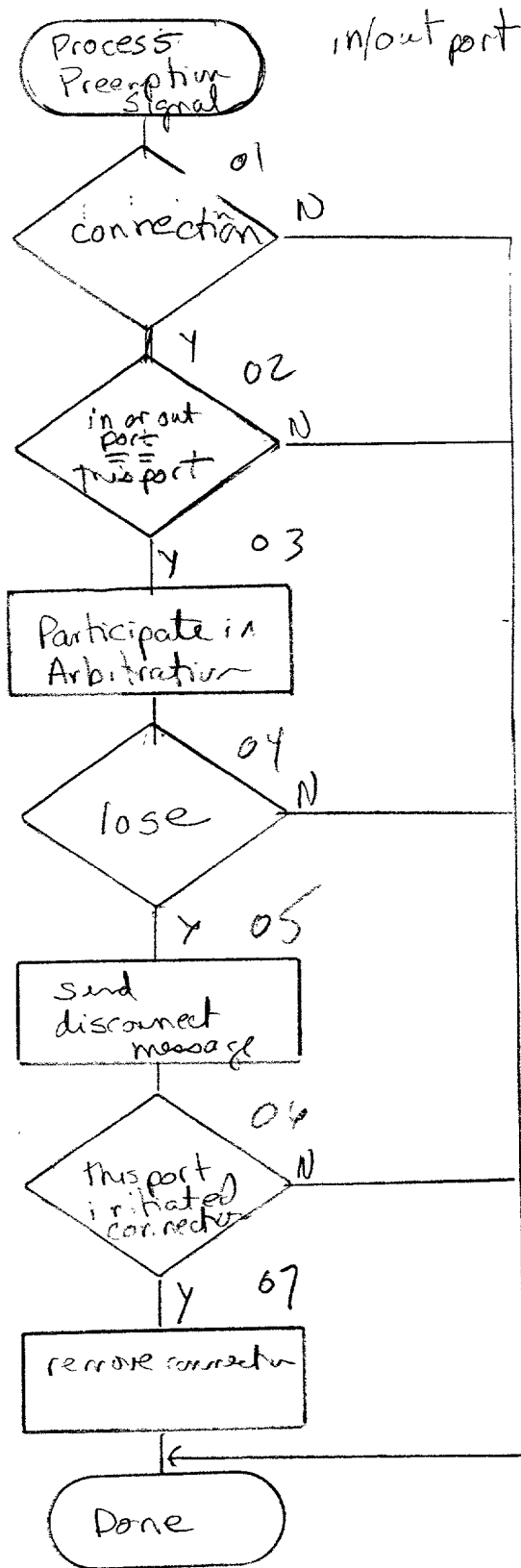


Fig 18

10069329 10601



10060329-102601

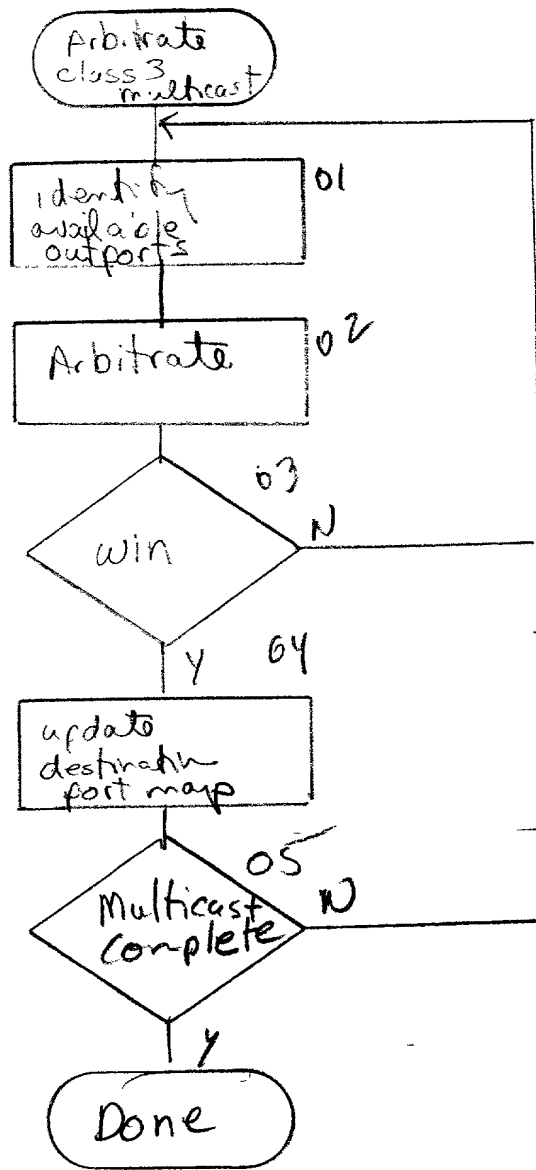


Figure 20